

PATENT NUMBER

SCANNED 15² O.A. A.G.

APPLICATION NO. 09/688203	CONT/PRIOR F	CLASS 365	SUBCLASS 2818	ART UNIT 2818 3823	EXAMINER
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is Kenichiro Nakagawa

Method of manufacturing semiconductor memory device

PTO-2040
12/89

ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION									
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) _____ (Date)			ISSUE FEE <div style="display: flex; justify-content: space-between;"> Amount Due Date Paid </div>	
<input type="checkbox"/> c) The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	
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(LABEL AREA)